

ABSTRACT OF THE DISCLOSURE

A method of fabricating a liquid crystal display (LCD). The gate electrodes or the source/drain electrode pairs of thin film transistors in the LCD are patterned by at least a first mask and a second mask. The gate electrodes or the source/drain electrode pairs at the boundary region defined by both the first and second masks are divided into a first portion patterned by the first mask and a second portion patterned by the second mask. The boundary region is divided incrementally or by square wave such that the area of each of the gate electrodes or the source/drain electrode pairs in the first portion increases along the boundary direction and in the second portion decreases along the boundary direction.